

## **AMENDMENTS TO THE CLAIMS**

This listing of claims will replace all prior versions, and listings, of claims in the application:

### **Listing of Claims:**

Claims 1-3 (Canceled)

Claims 4-7. (Canceled)

Claim 8. (Canceled)

1        Claim 9. (Original) A portable computer system, comprising:

2        an address decoder coupled to an address bus generating a latch control signal by decoding

3        an address of an output port accommodating power-on self-test codes;

4        a latch coupled to a data bus of the portable computer system latching the power-on self-test

5        codes from the data bus in response to the latch control signal;

6        an indicating device having a plurality of lighting devices indicating operating states of the  
7        portable computer system;

8        a controller generating an indicating control signal in response to the operating state; and

9        a selector sending either the indicating control signal or power-on self-test codes of the latch

10       to said indicating device.

1           Claim 10. (Original) The portable computer system of claim 9, with the latched power-on  
2           self-test codes being outputted to the indicating device when the address decoder translates the  
3           address of the output port for power-on self-test codes.

1           Claim 11. (Original) The portable computer system of claim 9, with the controller managing  
2           the selector to output the power-on self-test codes latched in the latch during the power-on self-test  
3           process.

1           Claim 12. (Original) The portable computer system of claim 11, further comprising a key  
2           input device coupled to the controller, said controller regulating the selector to output the power-on  
3           self-test codes held temporarily until a key input signal response from the key input device during  
4           the power-on self-test process.

1           Claim 13. (Original) The portable computer system of claim 12, with the key input device  
2           being a keyboard of the portable computer system.

1           Claim 14. (Original) The portable computer system of claim 13, with said selector being a  
2           multiplexer, the output of said multiplexer being controlled by the controller.

1           Claim 15. (Previously Presented) The portable computer of claim 14, with the lighting  
2           devices being a plurality of light emitting diodes displaying the power-on self-test codes in

3 accordance with an order of the power-on self-test process.

Claims 16-18 (Canceled)

Claims 19-21. (Canceled)

Claims 22-23 (Canceled)

Claim 24. (Canceled)

1 Claim 25. (Previously Presented) A computer, comprising:

2 an address decoder coupled to an address bus generating a latch control signal by decoding  
3 an address of an output port accommodating power-on self-test codes;

4 a latch coupled to a data bus of the computer latching the power-on self-test codes from the  
5 data bus in response to the latch control signal;

6 an indicating device having a plurality of lighting devices indicating operating states of the  
7 computer;

8 a controller generating an indicating control signal in response to the operating state; and

9 a selector sending either the indicating control signal or power-on self-test codes of the latch  
10 to said indicating device.